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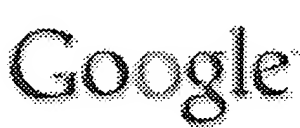




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		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L11	L10 and "SSL"	6
<input type="checkbox"/>	L10	L8 and "digital signal processor"	33
<input type="checkbox"/>	L9	L8 and "SSL session"	5
<input type="checkbox"/>	L8	L7 and "web browser"	175
<input type="checkbox"/>	L7	L6 and "security protocol"	443
<input type="checkbox"/>	L6	(digital signal processor or DSP) and (ASIC)	53134
<input type="checkbox"/>	L5	380/270.ccls.	550
<input type="checkbox"/>	L4	380/247.ccls.	233
<input type="checkbox"/>	L3	713/151.ccls.	256
<input type="checkbox"/>	L2	"digital signal processor" and "multi-layer protocol"	12
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<input type="checkbox"/>	L1	6088800.pn. 6212576.pn.	2

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shared memory, digital signal processor, multi

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[\[PDF\] Developing Embedded Software in Multi- Core SoCs](#)

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A common example is the use of **Digital Signal Processors** ... **Shared memory** is the most typical and versatile form for inter-processor communications. ...

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[D&R IP Interconnect Corner: Glossary of Terms - Agent, AHB, AMBA ...](#)

DSP (**Digital Signal Processor**): A DSP is a special-purpose CPU used for **digital**

... **Memory Controller**: A device that controls **shared memory** and accepts ...

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The ARM9 **processor** core uses a **multilayer** AHB structure to separate slow external ... The MC2301 PCI **digital-signal memory**-evaluation board targets ...

[www.edn.com/article/CA526330.html](http://www.edn.com/article/CA526330.html) - [Similar pages](#)

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Current technologies for **digital** multimedia **signal** processing in Internet applications. ... Topics include: integrating data and **shared memory** parallelism, ...

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D/A **Digital** to Analog DAA Data Access Arrangement + Decimal Adjust for Addition

DAC Data ... Exclusive, **Shared** and Invalid (**protocol**) MET **Memory** Enhancement ...

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Introduction to **digital signal** processing of continuous and discrete signals.

... large-scale multiprocessor systems. **Shared-memory** coherence and consis- ...

[www.engin.umich.edu/students/current/academics/courses/eecs.pdf](http://www.engin.umich.edu/students/current/academics/courses/eecs.pdf) - [Similar pages](#)

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Intr oduction to **digital signal** processing of continuous and discrete signals.

... rithms, parallel design automation on **shared memory** and distributed ...

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D/A **Digital** to Analog DAA Data Access Arrangement + Decimal Adjust for ...

Service Partner + **Digital Signal** Processing/**Processor** + **Digital Sound Processor** + ...

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BA Bell Atlantic (company) BACP Bandwidth Allocation Control **Protocol** BAK ...

Partner + **Digital Signal** Processing/**Processor** + **Digital Sound Processor** + ...

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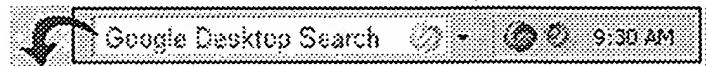
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**memory.** The ARM9 **processor** core uses a **multilayer** AHB ... **math-intensive** real-time algorithms. The MC2301 PCI **digital-signal memory-evaluation** ...

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